



H-1031

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

H. MIYAGAWA et al

Serial No. 10/086,718

Group Art Unit: 2858

Filed: March 4, 2002

Examiner: V. Nguyen

For: MODULATION SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND TESTING METHOD FOR OSCILLATION CIRCUIT

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 23, 2003

Sir:

It is respectfully requested that a Three-month Extension of Time, to and including October 23, 2003, be granted in which to respond to the Office Action dated April 23, 2003 in the above-identified application.

Our Credit Card Payment Form in the amount of \$950.00 is attached in payment of the appropriate fee.

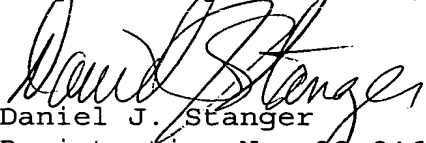
The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

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Respectfully submitted,


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